

## FACULTY PROFILE

**(Kindly give the information in plain text format. Do not send in tabular form)**



NAME : JAYEN MODI  
DESIGNATION : M.E. (Electronics), B.E. (Electronics)  
QUALIFICATION : Assistant Professor  
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AREA OF INTEREST : Analog Electronics, Electronic Devices & Circuits, Instrumentation & Control Systems, Automation, Mathematical Modeling & Simulation, Educational Technology & Pedagogy Research

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### PROFESSIONAL BACKGROUND :

- Assistant Professor in Electronics Engineering Department of Fr. Conceicao Rodrigues College of Engineering (Fr. CRCE) affiliated to University of Mumbai from 1<sup>st</sup> July 2014 till date (present)
- Assistant Professor in Electronics Engineering Department of Vidyalkar Institute of Technology (VIT), affiliated to the University of Mumbai from September 2010 till 30<sup>th</sup> June 2014
- Lecturer in the Electronics Engineering Department of Vidyalkar Institute of Technology (VIT), affiliated to the University of Mumbai from July 2007 till August 2010 – Full Time
- Lecturer in Electronics & Communication Engineering Department of Vidyalkar Polytechnic (VP), affiliated to MSBTE from June 2006 to May 2007 – Full Time
- Lecturer at Agnel Technical College (Polytechnic), autonomous institute of Maharashtra Government from July 2005 till May 2006 – Visiting Faculty
- Consultant, counsellor, teaching faculty & academic advisor at Vidyalkar Classes & Publications (VCP) from July 2003 to July 2014
- **Visiting Faculty, Mukesh Patel School of Technology Management & Engineering (MPSTME), Mumbai under SVKM's NMIMS University, deemed-to-be u/s 3 of UGC Act, 1956 in 2012**

### EDUCATIONAL DETAILS :

- Passed **M.E. (Electronics Engineering)** with First Class in March 2008 from the Fr. Conceicao Rodrigues College of Engineering, Mumbai affiliated to the University of Mumbai (State University)
- Passed **B.E. (Electronics Engineering)** with First Class in May 2005 from Shah & Anchor Kutchhi College of Engineering, Mumbai affiliated to the University of Mumbai (State University)
- Passed **Diploma in Industrial Electronics** from Vivekanand Education Society's Polytechnic, Mumbai in May 2002, affiliated to the Maharashtra State Board of Technical Education (MSBTE)
- Completed schooling (**SSC**) from St. Joseph's High School, Mumbai in March 1999 with Distinction affiliated to Maharashtra State Board of Secondary & Higher Secondary Education (Mumbai Board)

### ADMINISTRATIVE BACKGROUND :

- Lab In-charge of Project Lab at Electronics Engineering Department of Fr. Conceicao Rodrigues College of Engineering (Fr. CRCE) 1<sup>st</sup> July 2014 till date (present)
- Lab In-charge of Electronic Devices & Circuits laboratory in Electronics Engineering Department of Vidyalkar Institute of Technology (VIT), affiliated to the University of Mumbai from September 2009 till 30<sup>th</sup> June 2014

- Handled all First Year (F.E.) Engineering admissions at ARC centre as counsellor & co-ordinator at Vidyalankar Institute of Technology (VIT) from 2007 till 2012
- Coordinator for staff & students industrial visits both in India & abroad at Vidyalankar Institute of Technology (VIT) from 2008 to 2013

Ph.D. SUPERVISED :

RESEARCH GRANT PROJECTS/PATENTS :

PUBLICATIONS :

- Jayen Modi & Rahul Sarangle, 'Brain Computer Interface – New Invention in Brain Sciences' by Lambert Academic Publishing – Germany; ISSN No. 978-3-8484-4228-7
- Jayen Modi, Rahul Sarangle & Uday Pandit Khot, 'GSM Based Power Meter Reading & Control System' in International Journal of Electronic Communication & Computer Engineering (IJECCCE) of volume – 3, issue – 4, July 2012; ISSN No. 2249-071X
- Jayen Modi, 'GSM Based Power Meter Reading & Control System', in International Journal of Engineering Research & Applications (IJERA) of July 2012; ISSN No. 2248-9622
- Jayen Modi, 'Rapid Prototyping Technology (RPT) – Process Techniques & Applications' of the National Conference on Innovations in Electronics & Information Technology by Vidyalankar Institute of Technology (VIT), Mumbai in October 2009
- Jayen Modi, Saket Kochhar & Obro Mukherjee, 'Radio Frequency Identification (RFID)' of the National Conference on Innovations in Electronics & Information Technology by Vidyalankar Institute of Technology (VIT), Mumbai in October 2009
- Jayen Modi, Mojes Koli & Nitin Chauhan, 'Smart Antennas in Wireless Communications' of the National Conference on Innovations in Electronics & Information Technology by Vidyalankar Institute of Technology (VIT), Mumbai in October 2009
- Jayen Modi, Naresh Bomiseti & Devdutt Haldipur, 'To Generate Optimal Solution for Complex Circuits by Simplex Method' of National Conference on Innovations in Electronics & Information Technology by Vidyalankar Institute of Technology (VIT), Mumbai in October 2009
- Jayen Modi, Ganesh Chandrashekharan & Kharveel Parakh, 'Mathematical Modeling of Adaptive Control' of National Conference on Innovations in Electronics & Information Technology by Vidyalankar Institute of Technology (VIT), Mumbai in October 2009

BOOKS AUTHORED :

TRAINING PROGRAMS CONDUCTED :

PARTICIPATION IN TRAINING PROGRAM :

- One-day seminar on Power Electronics, organized by Electronics Engineering Department of the Fr. Conceicao Rodrigues College of Engineering, Bandra (Mumbai) on 18<sup>th</sup> March 2006
- Two-day seminar on Recent Developments in Biomedical Instrumentation, 31<sup>st</sup> August 2007 & 1<sup>st</sup> September 2007 organized by Electronics Engineering Department of Fr. Conceicao Rodrigues College of Engineering, Bandra (Mumbai)
- Three-days ISTE sponsored workshop on MATLAB Applications in Engineering from 9<sup>th</sup> January 2008 to 11<sup>th</sup> January 2008 at Vidyalankar Institute of Technology (VIT), Mumbai
- Three-days ISTE approved workshop on Nanotechnology Applications in Biomedical & Electronic Engineering at Vidyalankar Institute of Technology (VIT), Mumbai from 12<sup>th</sup> to 14<sup>th</sup> January 2009
- Two-week AICTE & MHRD sponsored Short Term Course on Mechatronics & Robotics by the Mechanical Engineering Department of National Institute of Technology (NIT) Hamirpur (H.P.) from 12<sup>th</sup> to 21<sup>st</sup> July 2008
- One-week AICTE sponsored STTP on Nanotechnology – Sustainable Alternative to Environment organized by Chemical Engineering Department of Sardar Vallabhbhai National Institute of Technology (SVNIT), Surat from 19<sup>th</sup> to 23<sup>rd</sup> January 2009

- One-week ISTE approved STTP on Signal Processing & Applications organized by Department of Electronics & Telecommunication Engineering of Vidyalankar Institute of Technology (VIT), Mumbai from 6<sup>th</sup> to 12<sup>th</sup> July 2009
- One-week AICTE & MHRD sponsored Short Term Course on Advances in Optical & Wireless Communication by Electronics & Communication Engineering Department of National Institute of Technology (NIT) Hamirpur from 14<sup>th</sup> to 18<sup>th</sup> December 2009
- One-week ISTE approved STTP on Advances in the Embedded Systems, VLSI & MEMS Technologies organized by Department of Electronics Engineering of Vidyalankar Institute of Technology (VIT), Mumbai from 12<sup>th</sup> to 16<sup>th</sup> July 2010
- One-week ISTE approved STTP on the Recent Trends in Digital Signal Processing (DSP) & its Applications organized by Department of Electronics Engineering of Vidyalankar Institute of Technology (VIT), Mumbai from 11<sup>th</sup> to 15<sup>th</sup> July 2011
- Two-week ISTE approved STTP on Simulation – Concepts to Practice organized by Department of Electronics Engineering of Fr. Conceicao Rodrigues College of Engineering, Bandra (Mumbai) from 14<sup>th</sup> to 25<sup>th</sup> May 2012
- One-week Short Term Faculty Development Programme (FDP) on Numerical & Computational Techniques in Engineering organized by Department of Civil Engineering of Malaviya National Institute of Technology (MNIT), Jaipur from 17<sup>th</sup> to 24<sup>th</sup> December 2012
- One-week Short Term Training Programme (STTP) on Recent Trends in VLSI & Communication Systems (RTVCS) organized by Electronics & Communication Engineering Department, National Institute of Technology (NIT) Hamirpur, from 10<sup>th</sup> to 14<sup>th</sup> June 2013
- One-week Short Term Training Programme (STTP) on Advances in Control & Instrumentation Education & Implementation organized by the Electrical Engineering Department, Sardar Vallabhbhai National Institute of Technology (SVNIT) Surat, from 18<sup>th</sup> to 22<sup>nd</sup> May 2015
- One-week Faculty Development Programme (FDP) on LabVIEW & Application Hardware Integration organized by the Electronics Engineering Department at Sardar Patel Institute of Technology (SPIT) from 30<sup>th</sup> November to 5<sup>th</sup> December 2015
- One-week IEEE Robotics & Automation Society (RAS) Summer School on Multi-robot Systems organized by School of Computing (SoC), National University of Singapore (NUS) at the Republic of Singapore from 6<sup>th</sup> to 10<sup>th</sup> June 2016
- One-week Short Term Training Programme (STTP) on Digital VLSI Design organized by the Electrical Engineering Department, Sardar Vallabhbhai National Institute of Technology (SVNIT) Surat, from 19<sup>th</sup> to 23<sup>rd</sup> December 2016

#### INTERACTION WITH OUTSIDE WORLD (RESOURCE PERSON FOR GUEST LECTURE) :

- 'Signal Processing & Applications' from 6<sup>th</sup> July to 12<sup>th</sup> July 2009 at the Vidyalankar Institute of Technology (VIT) for a laboratory session on MATLAB applications
- 'Electronic Circuit Design & Simulation' from 18<sup>th</sup> June to 20<sup>th</sup> June 2013 at Shoolini University of Biotechnology & Management Sciences, Solan (H.P.) on concepts of circuit simulation
- 'Global Country Study' on 12<sup>th</sup> April 2014 for students on management programme (MBA) at the SMJV's CKSV Institute of Management, Baroda (Gujarat)
- 'Workshop on GATE Coaching' on 12<sup>th</sup> January 2014 for external students at Shree Sahajanand Institute of Management (SSIM) at Bhavnagar (Gujarat)
- 'Ethics & Ethical Issues in Engineering & Research' on 24<sup>th</sup> September 2015 for post-graduate students at Vidyalankar Institute of Technology (VIT), Mumbai
- 'Mathematical Modeling & Simulation' on 8<sup>th</sup> July 2016 for students of Electrical, Electronics & Communication Engineering at Shri Swami Atmanand Saraswati Institute of Technology, Surat

PROFESSIONAL AFFILIATIONS : Life Member – ISTE

OTHERS DETAILS : **Visited Multimedia University, Malaysia & National University of Singapore (NUS) on educational visit in 2011**